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FIG. 1

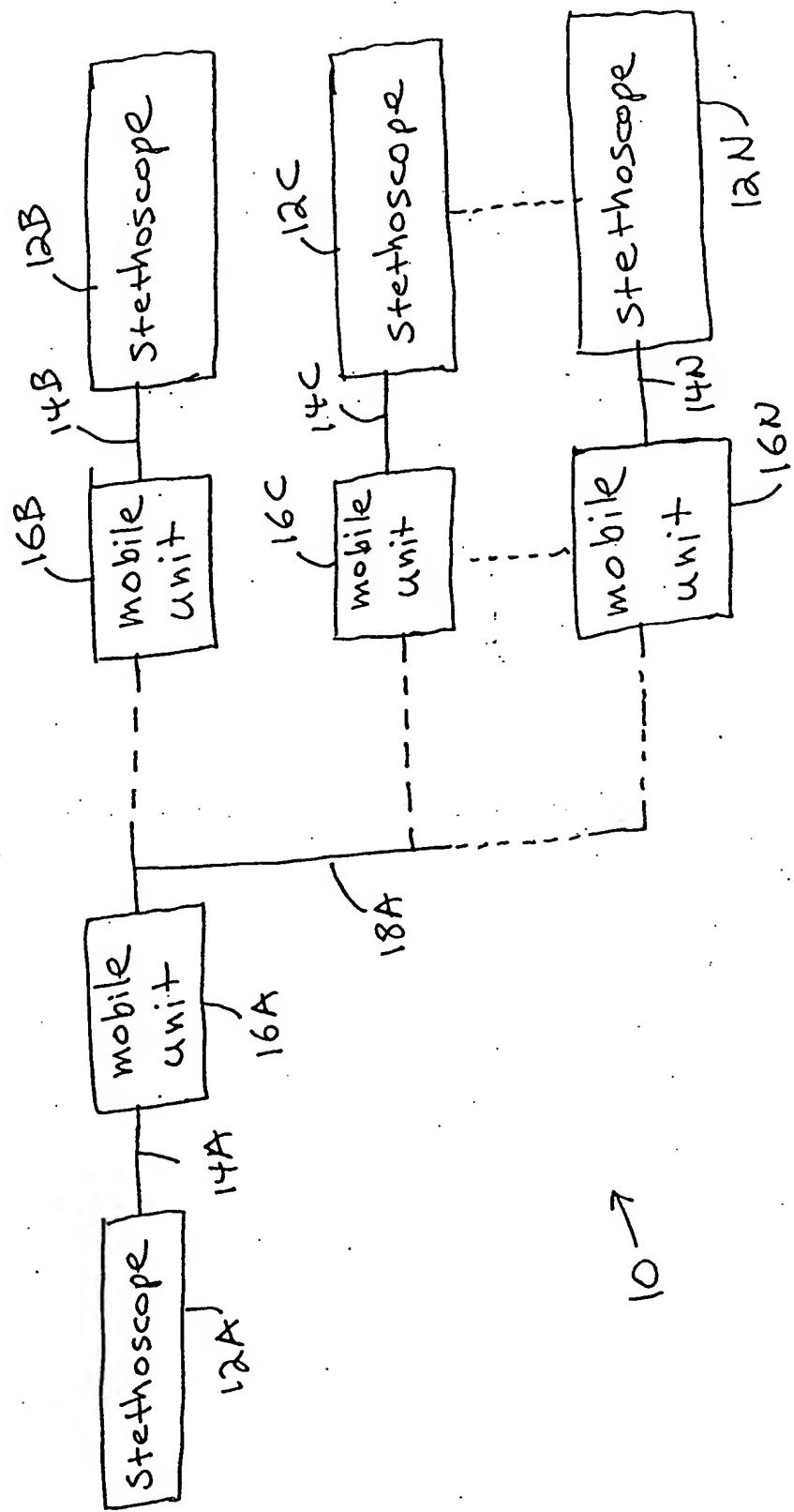


FIG. 2A

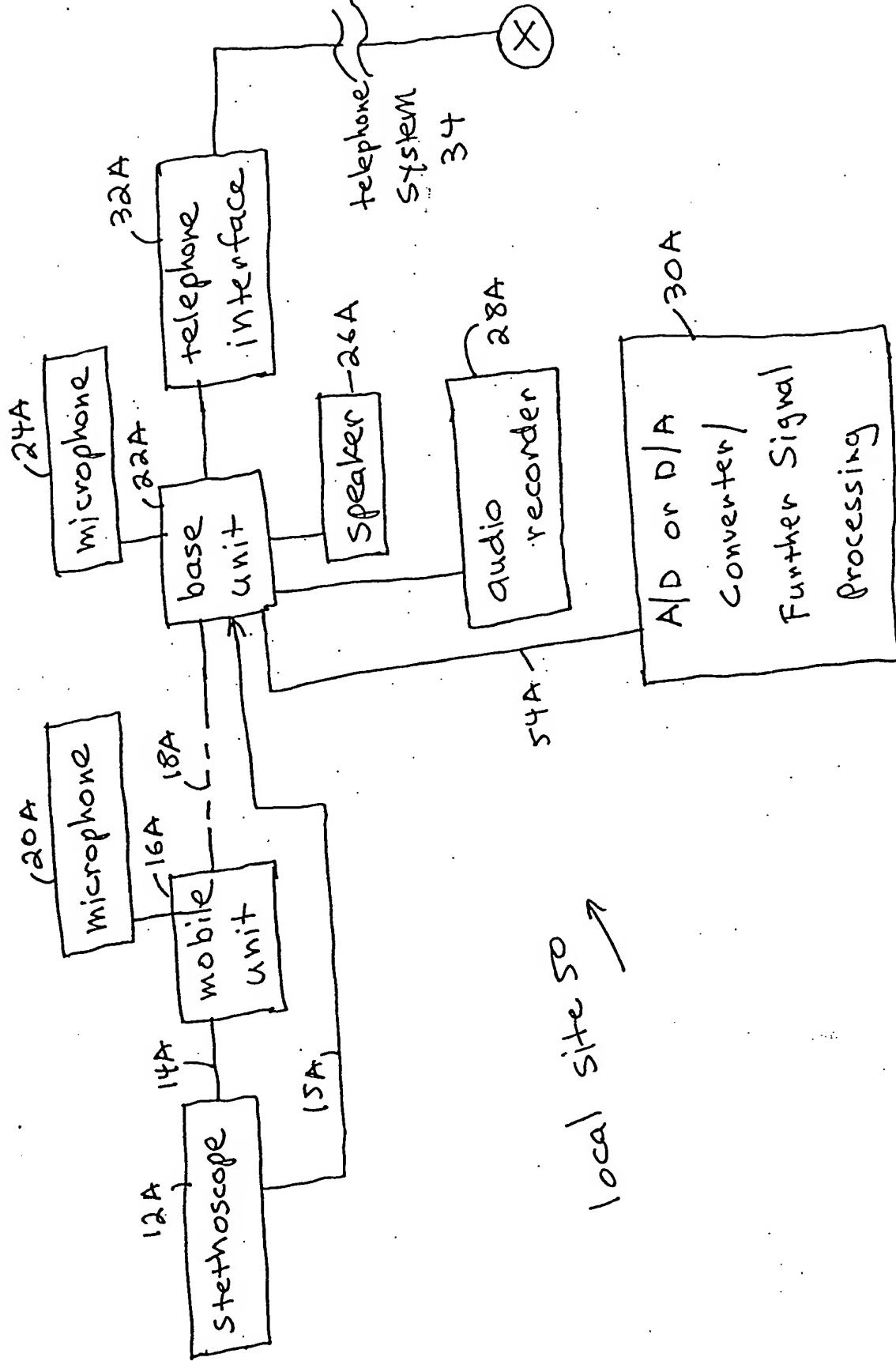
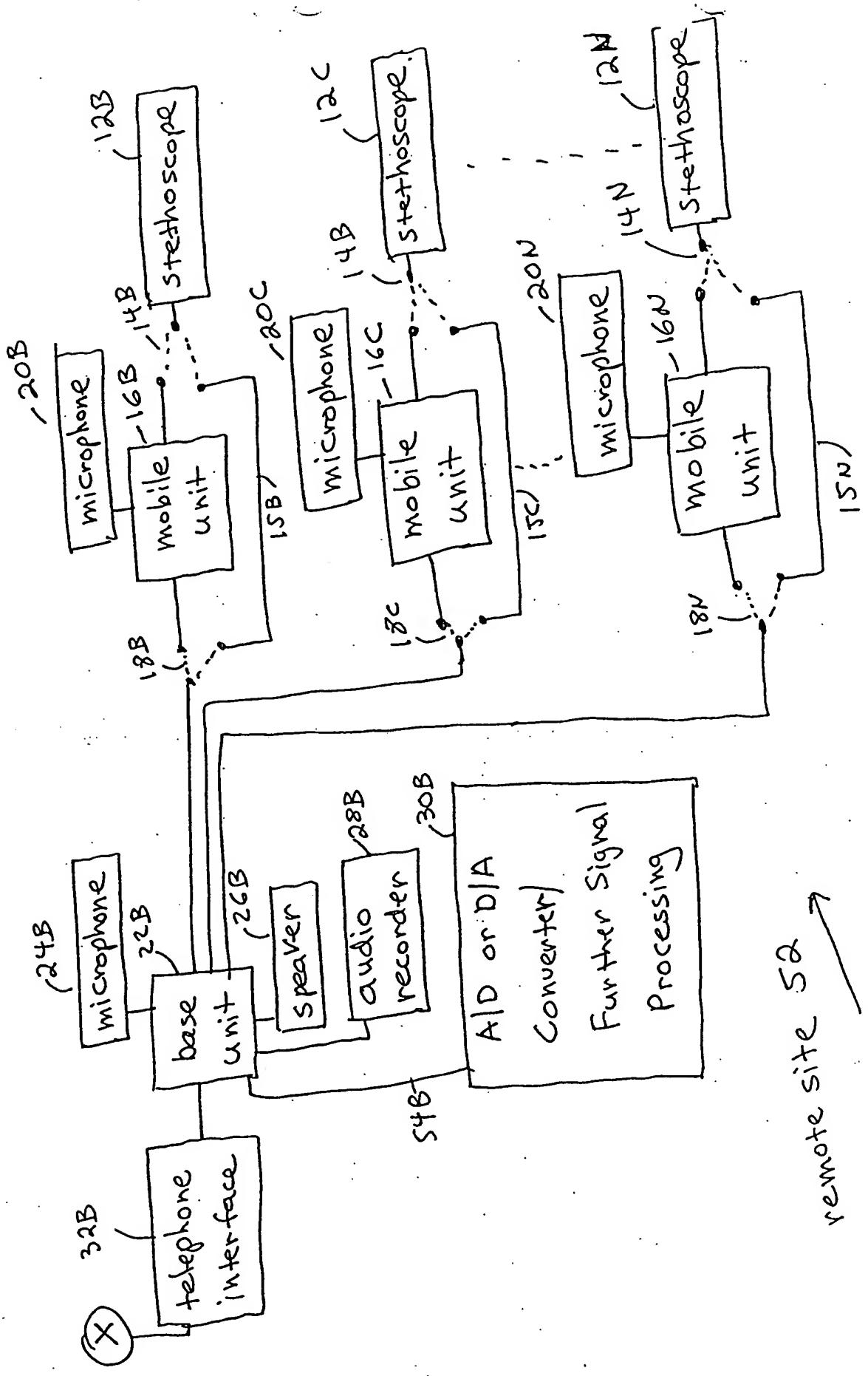


FIG. 2B



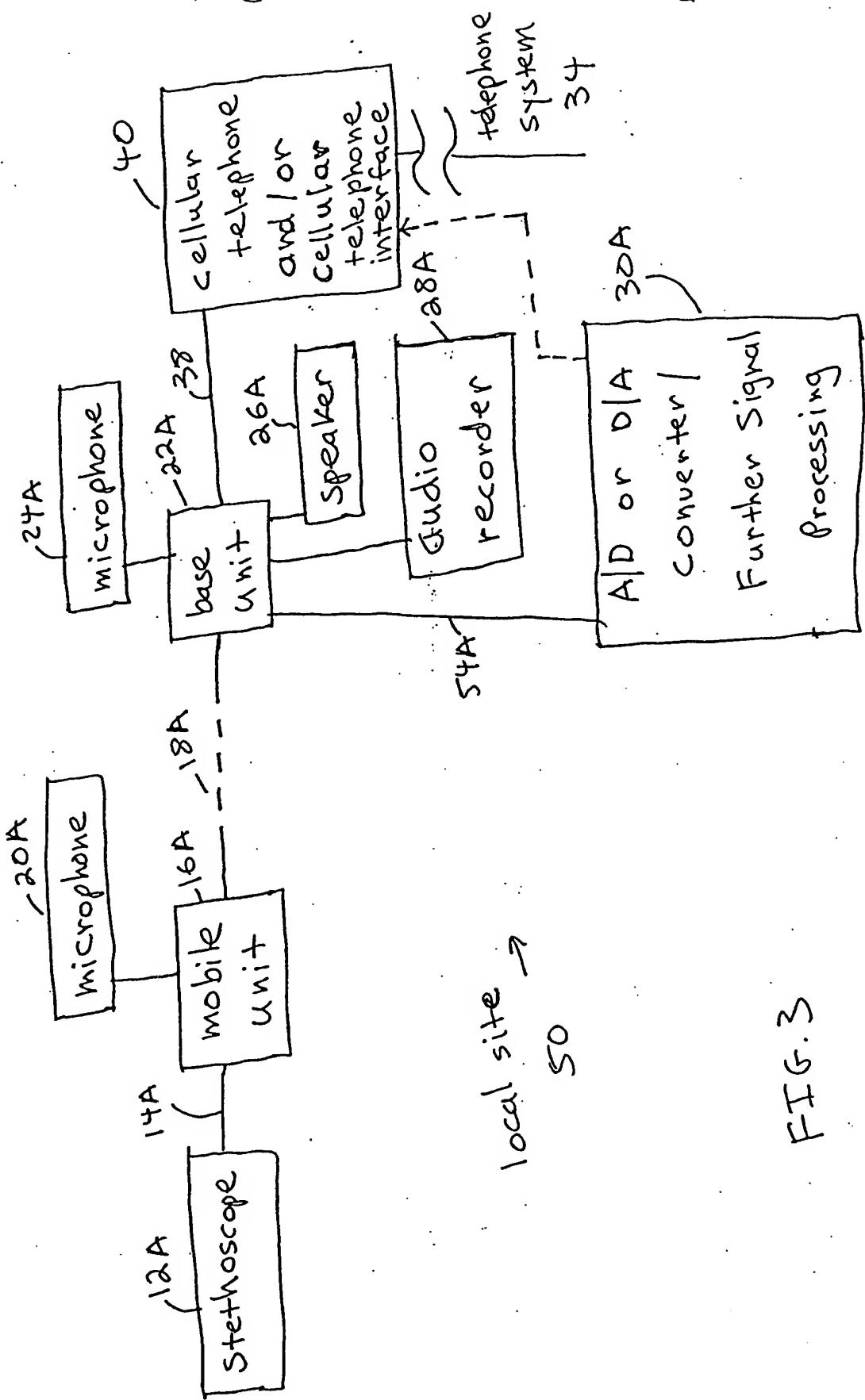
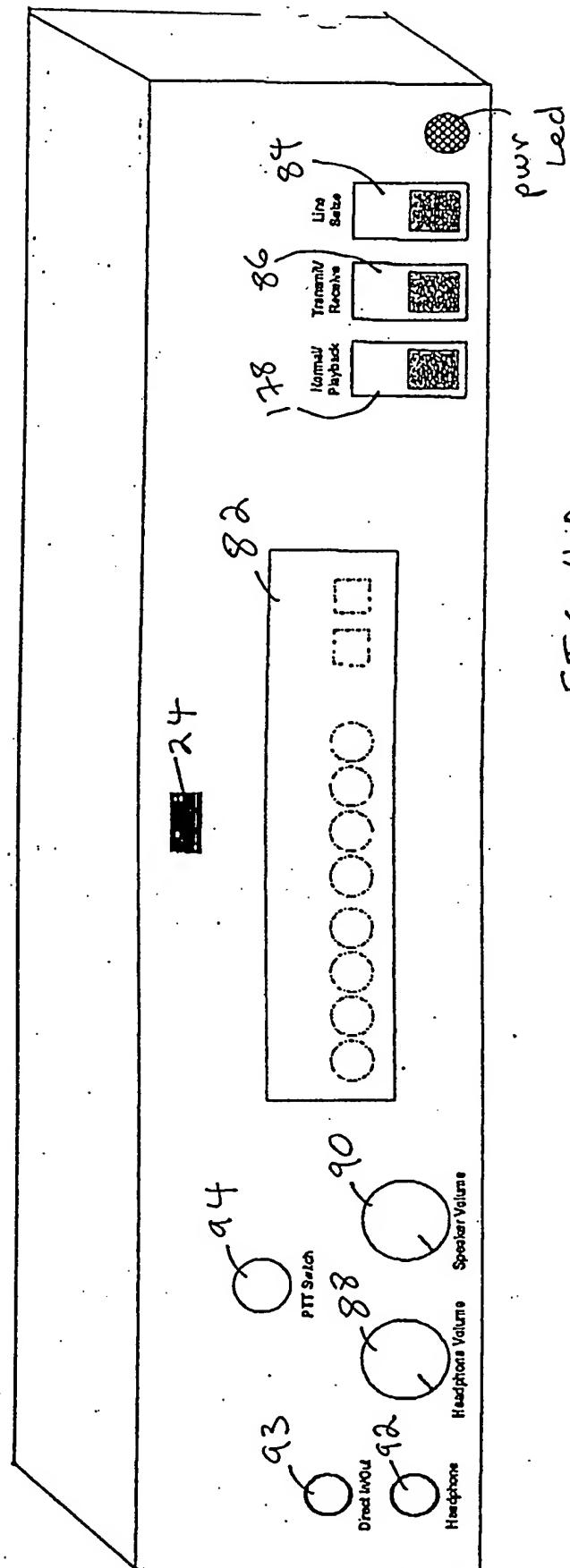
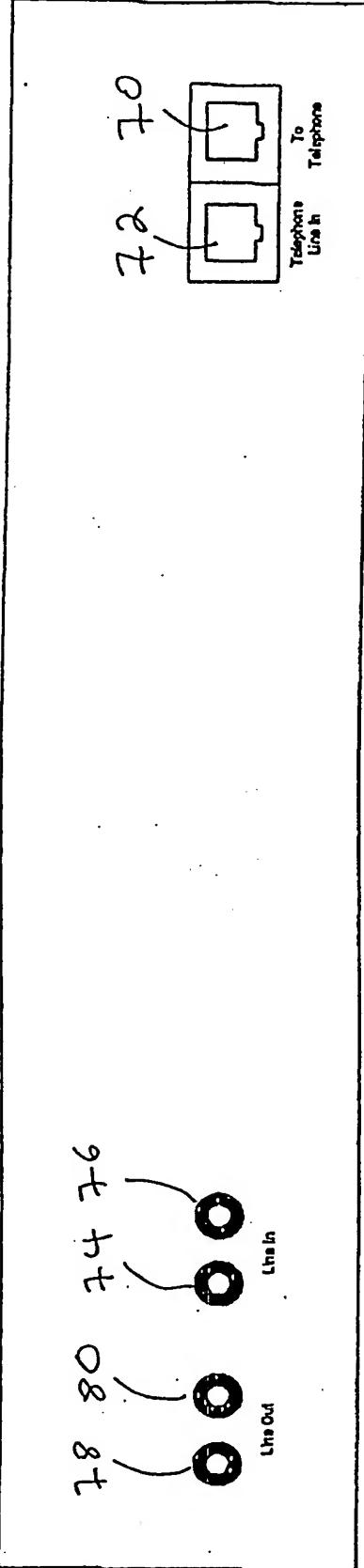


FIG. 3

Rear View FIG. 4 B



Front View FIG. 4 A

FIG. 5A

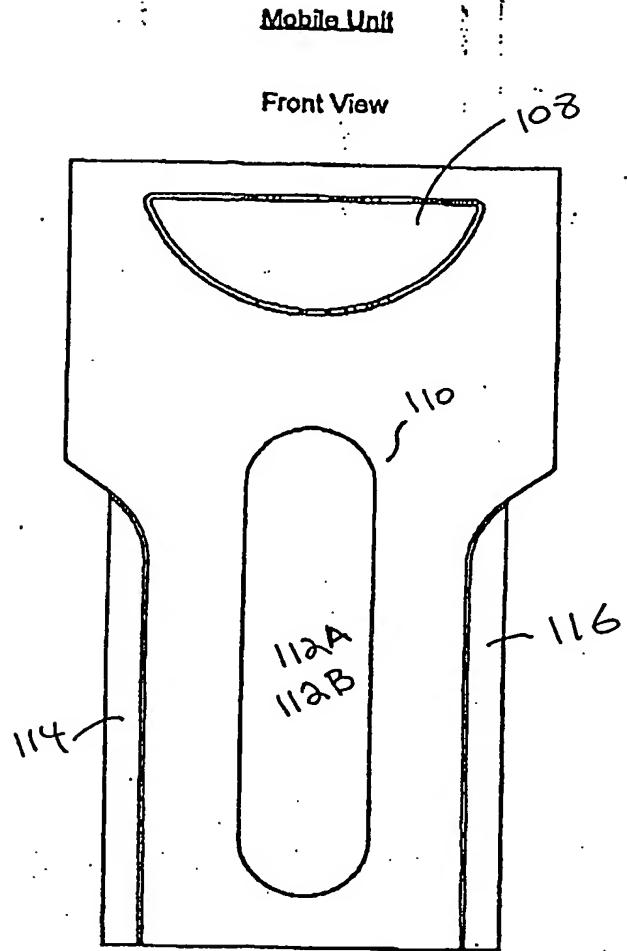


FIG. 5B

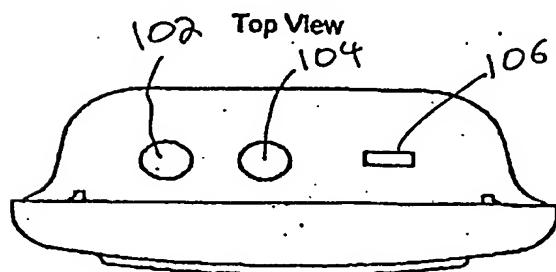


FIG. 6

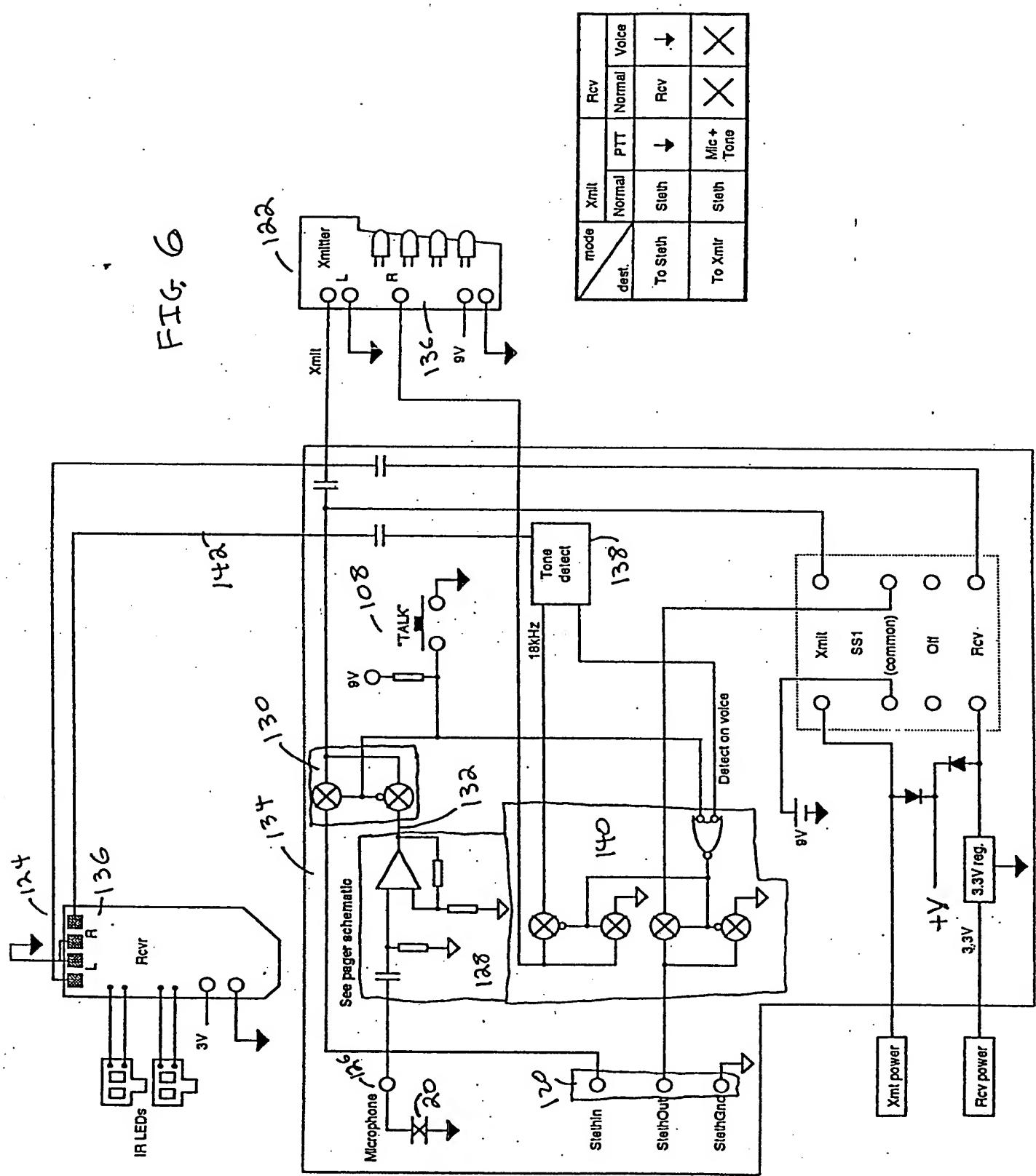
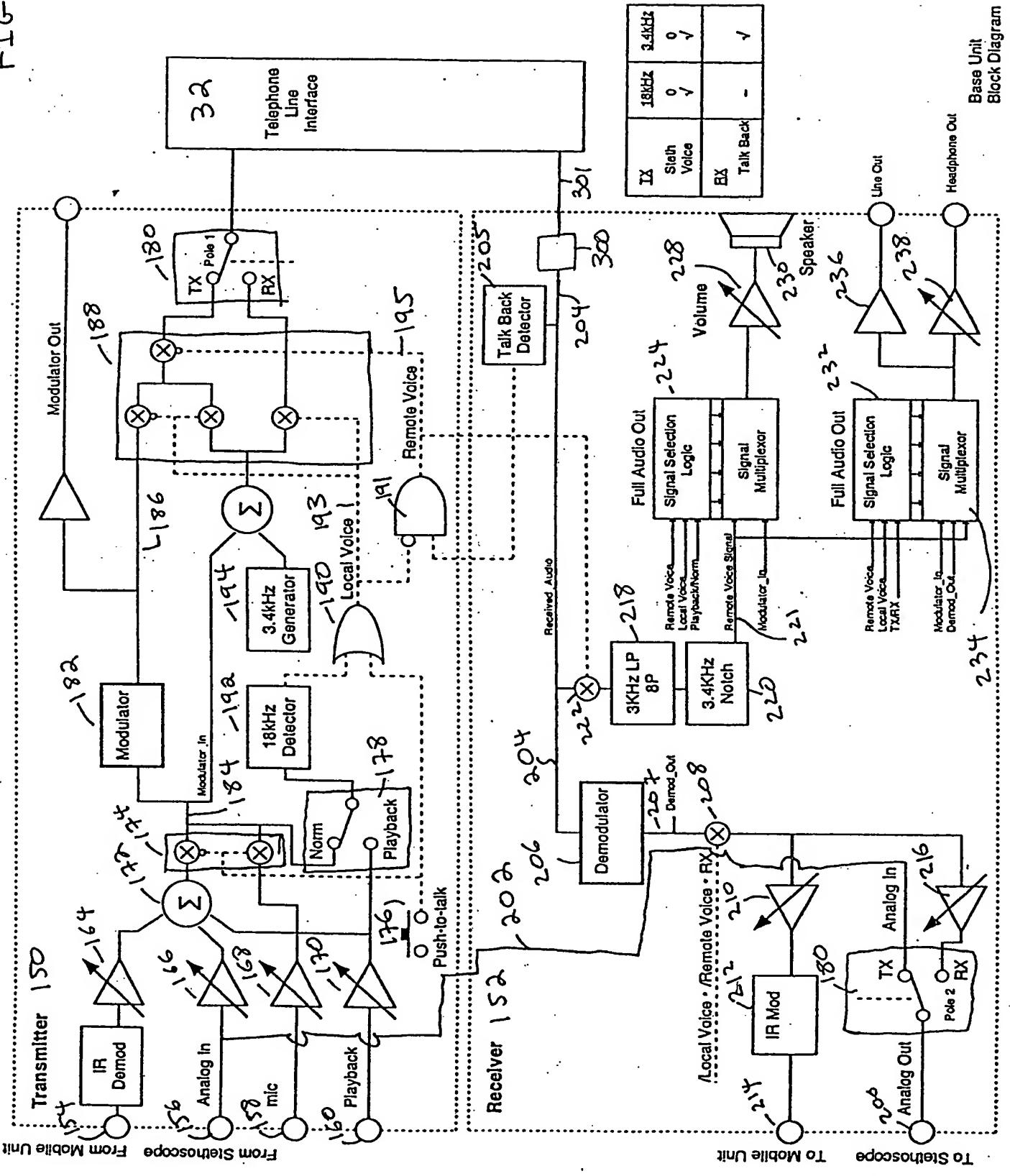


FIG 7



Modulator/Demodulator  
Block Diagram

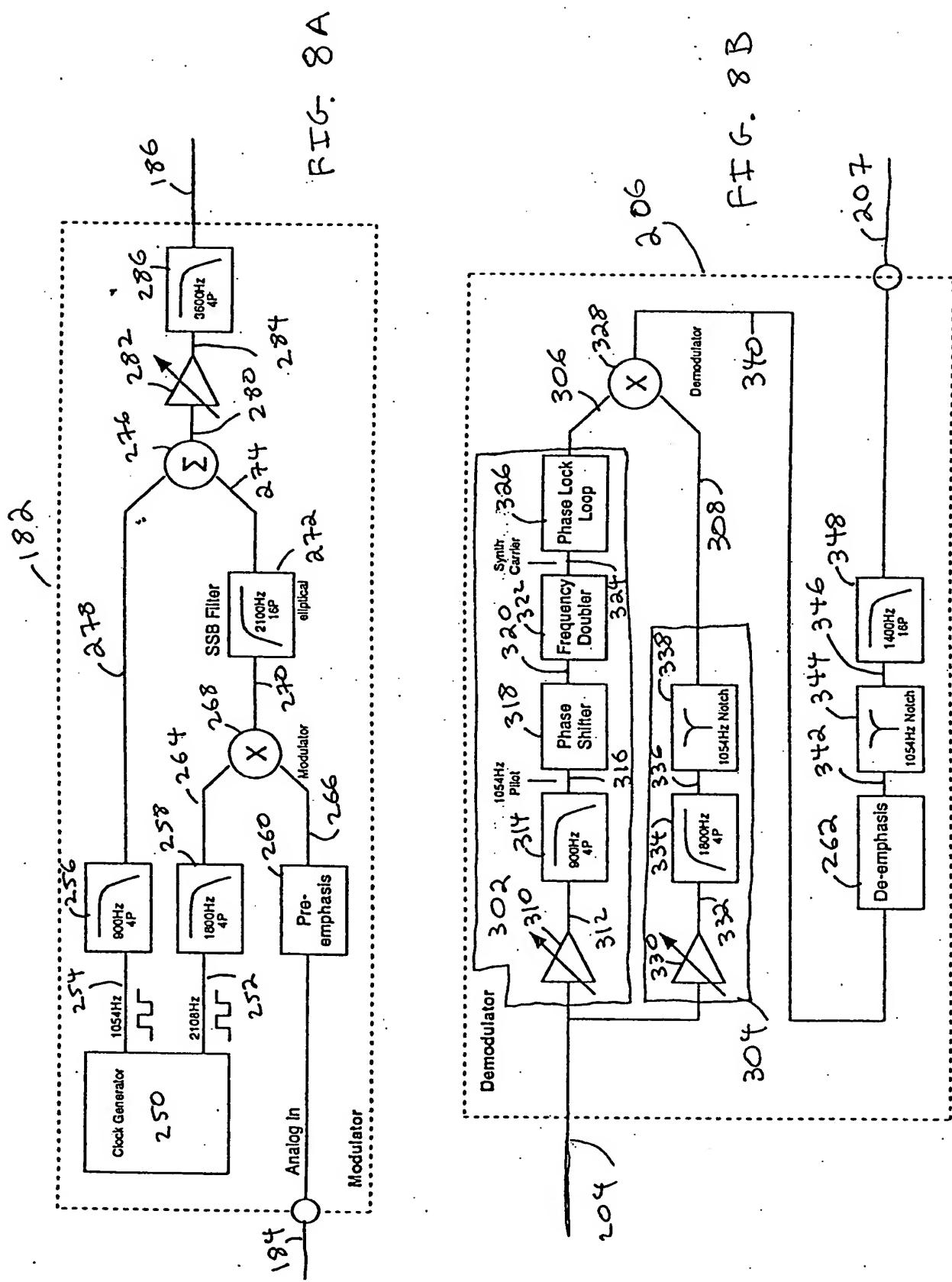
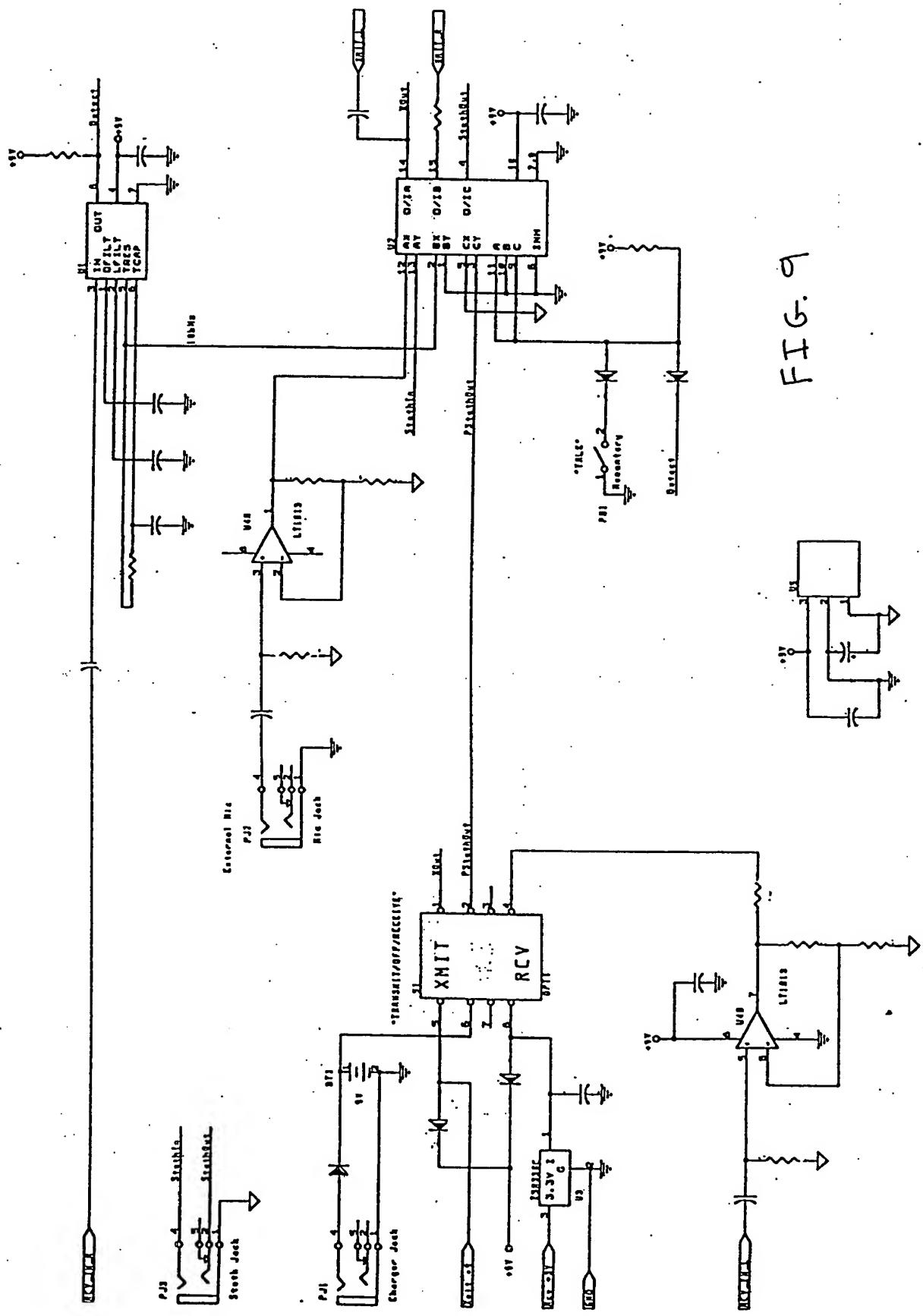
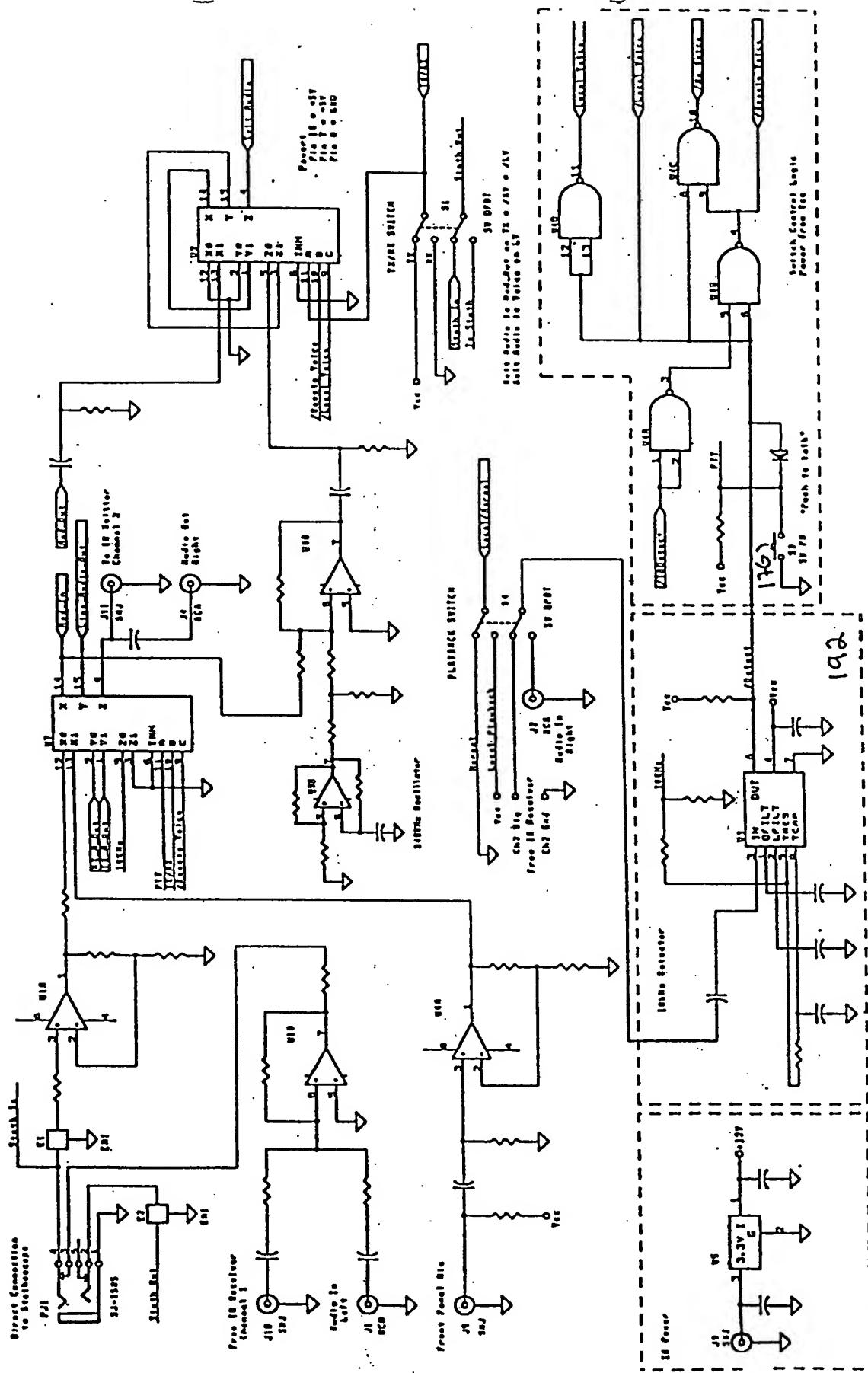


FIG. 9



FITG. 10



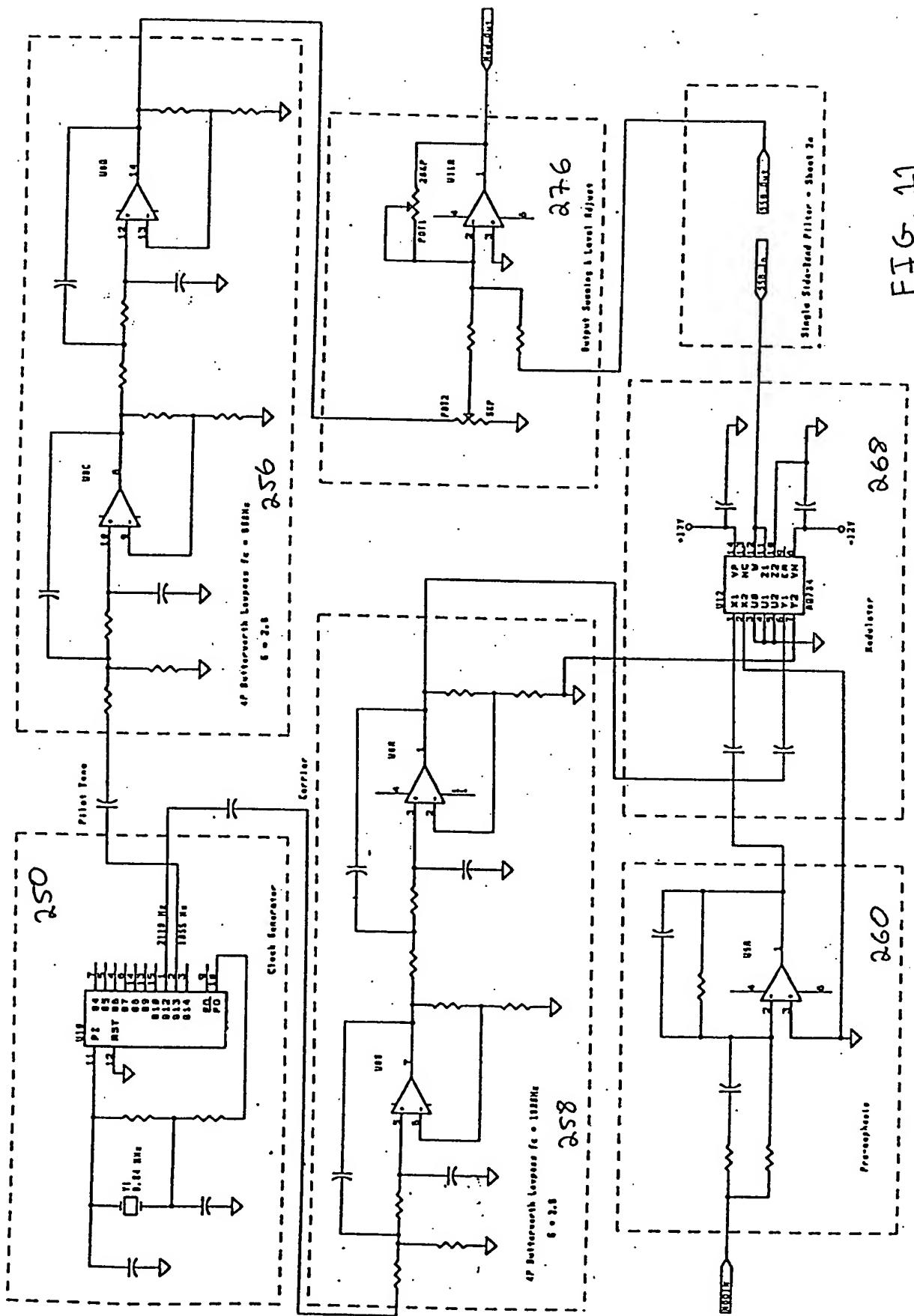
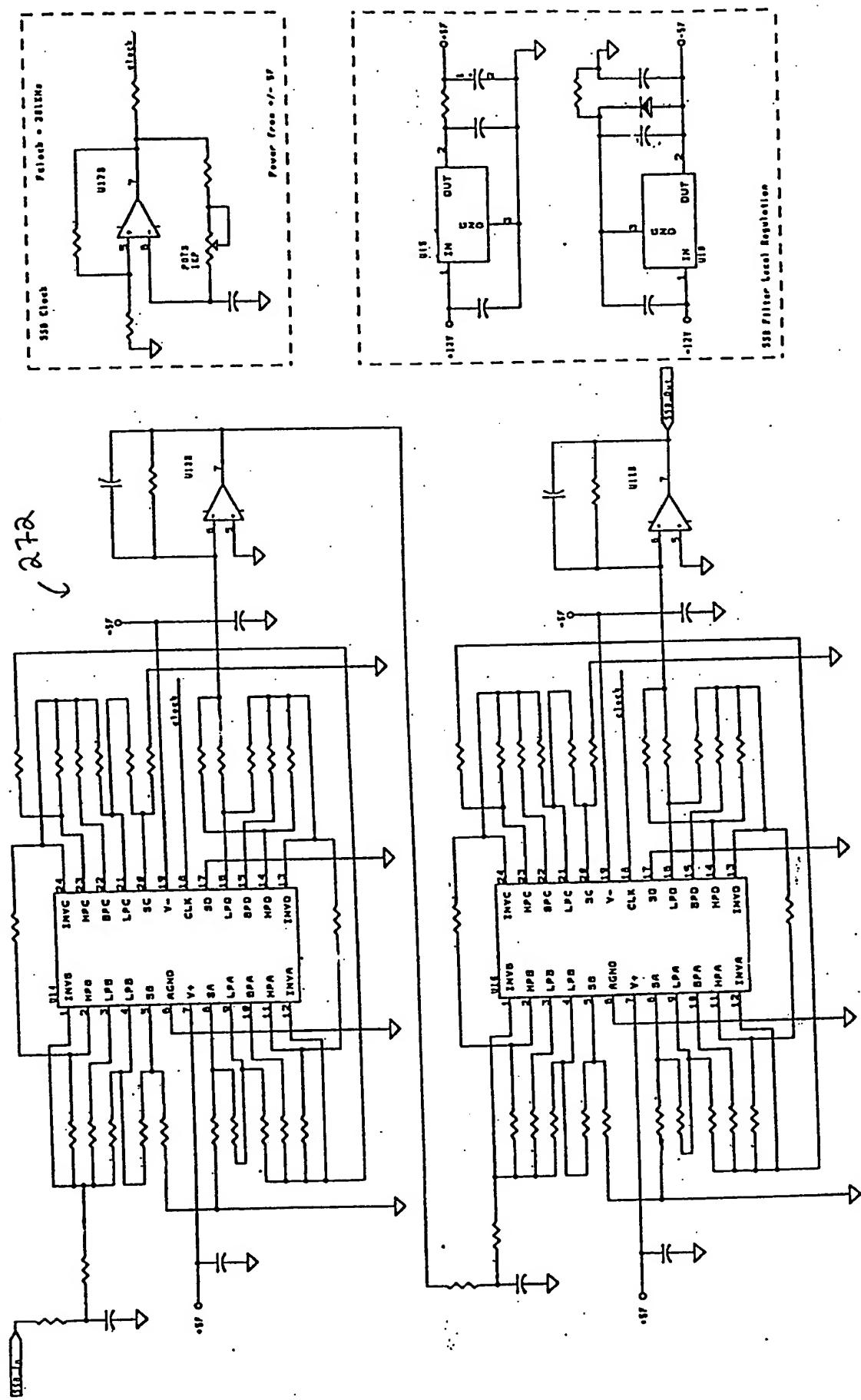


FIG. 12



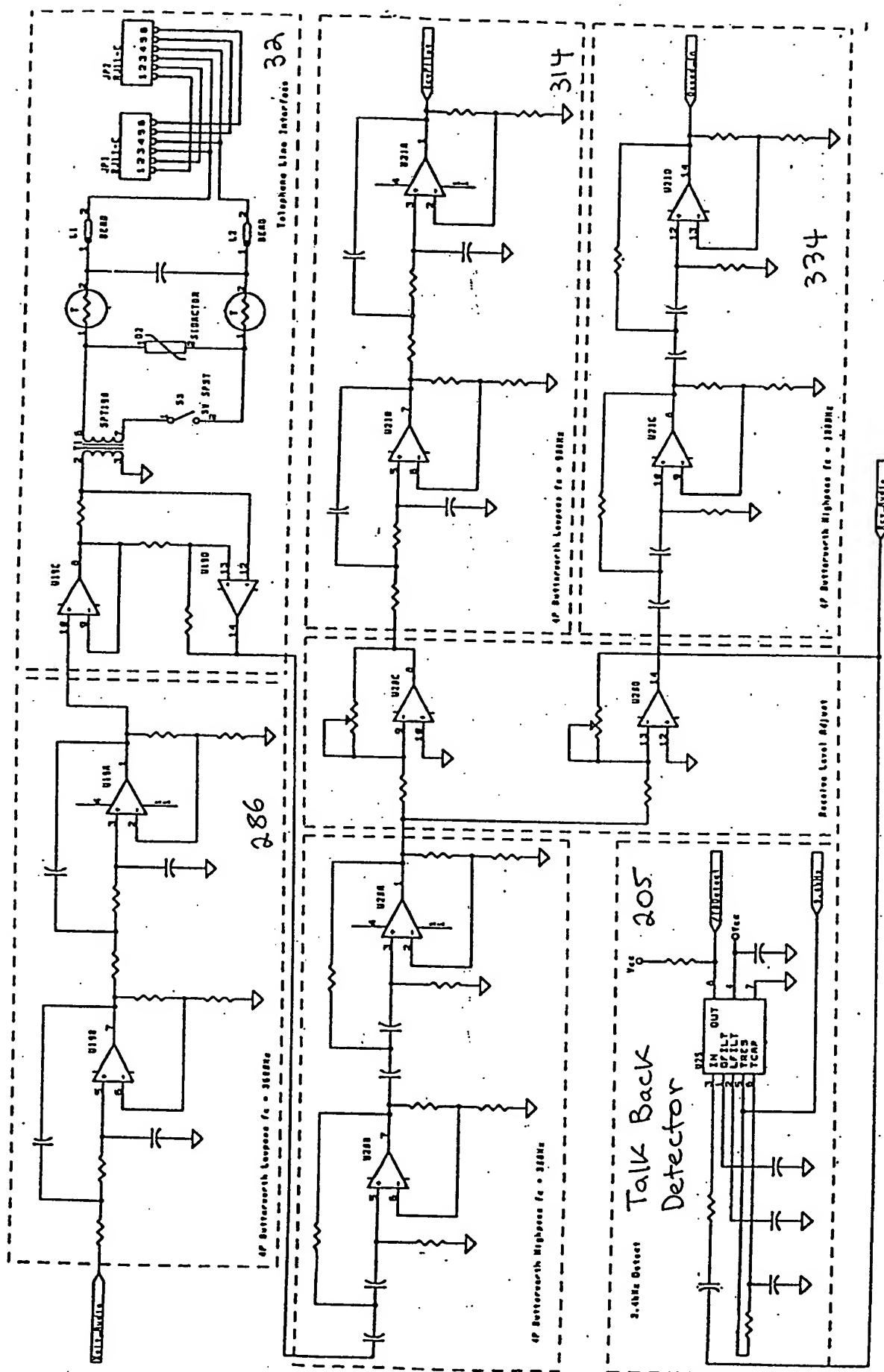
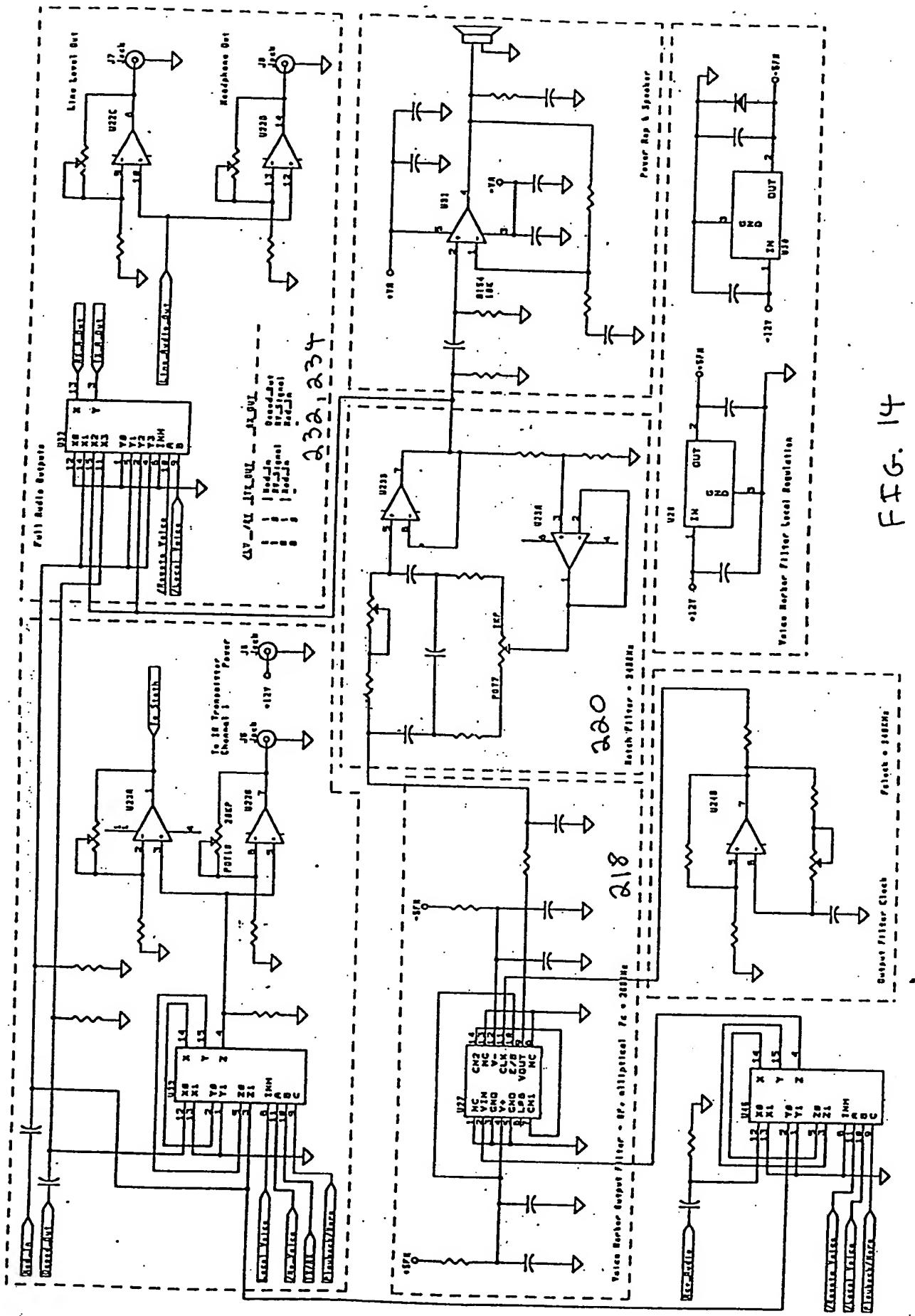
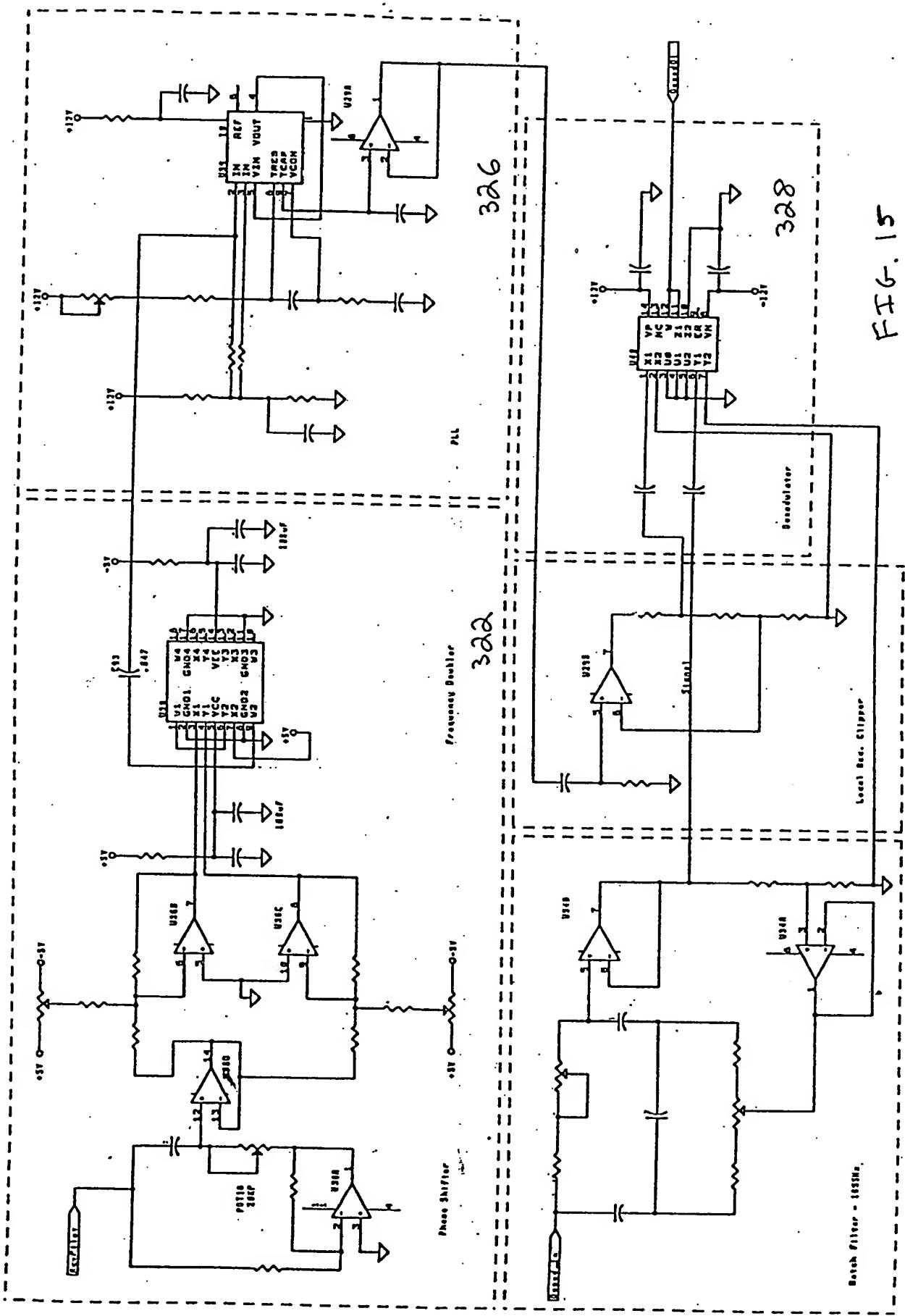


FIG. 13



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FIG. 15



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FIG. 16

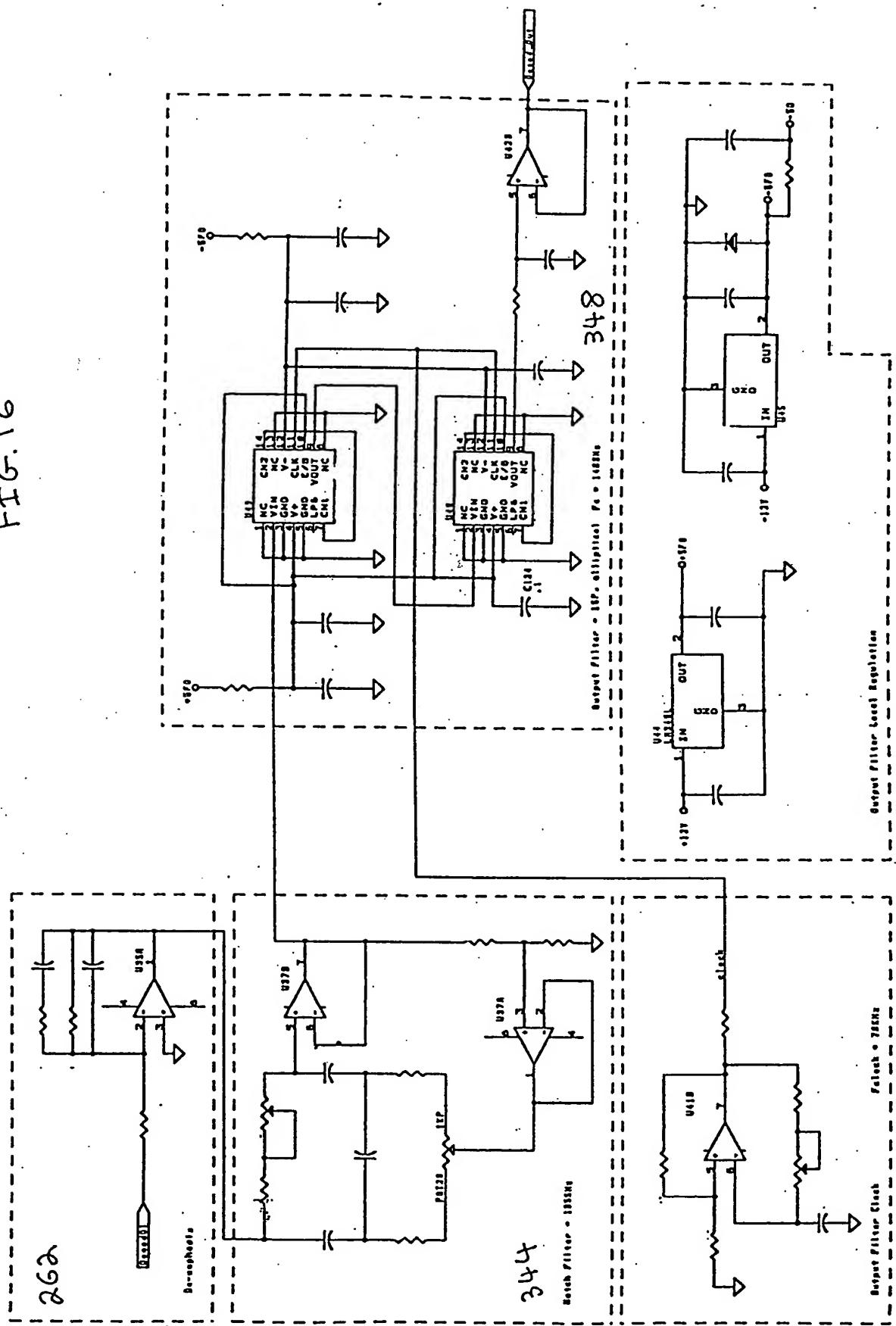


FIG. 17

